

10 | 808,785

EAST History			7/15/2005	10:20 AM
BRS	64	differential adj conversion adj circuit	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	15-Jul-05
IS&R	2091	(326/115,121,122,127,129).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	15-Jul-05
BRS	136	S2 and (current adj mirror)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	15-Jul-05
IS&R	166041	(("326") or ("327") or ("330")).CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	15-Jul-05
BRS	4790	S4 and (duty adj cycle)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	15-Jul-05
BRS	497	S5 and (current adj mirror)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	15-Jul-05
BRS	488	S6 and input and output	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	15-Jul-05
BRS	294	S7 and differential	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	15-Jul-05